

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	1347	(716/2-3).CCLS.	US-PGPUB; USPAT	OR	OFF	2005/04/19 14:26
S48	349	(716/3).CCLS.	USPAT	OR	OFF	2005/04/19 09:22
S49	802	(716/6).CCLS.	USPAT	OR	OFF	2005/02/24 07:57
S50	702	bypass and (fsm or finite near3 state or conditional near3 state or cse) and (delay or buffer) and (mux or multiplex\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 08:00
S51	51	S48 and S49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 07:58
S52	1100	S48 S49	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 07:58
S53	3	S50 and S52	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 07:59
S54	541	(fsm or finite near3 state or conditional near3 state or cse) same (equivalent or redundant)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 08:01
S55	70	S54 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 08:47
S56	626	bypass adj2 logic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 08:48
S57	2	(bypass adj2 logic) same fsm	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 08:49

S58	23	(bypass adj2 logic) same (fsm or finite or conditional)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 11:49
S59	19	whitmore-s\$.xa. and intel\$.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/02/24 11:49
S60	19201	(condition\$2 adj2 element or flip near2 flop or latch or register or cache) same (redundant or equivalent)	USPAT	OR	OFF	2005/04/19 09:23
S61	32706	(condition\$2 adj2 element or flip near2 flop or latch or register or cache) same (redundant or equivalent)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/19 09:24
S62	424	S61 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/19 14:26